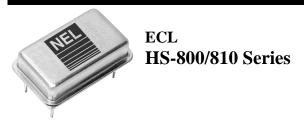
Rev. K



Description

The **HS-800/810 Series** of quartz crystal oscillators provide MECL 10K and 10KH series compatible signals in industry standard four-pin DIP hermetic packages.. Systems designers may now specify space-saving, cost-effective packaged ECL oscillators to meet their timing requirements.

Features

- Wide frequency range-15.0MHz to 250.0MHz
- User specified tolerance available
- Will withstand vapor phase temperatures of 253°C for 4 minutes maximum
- Space-saving alternative to discrete component oscillators
- High shock resistance, to 3000g
- All metal, resistance weld, hermetically sealed package

Electrical Connection HS-800

Pin Connection

1 N.C.

7 V_{CC}/Ground

8 Output

14 V_{EE} -5.2V

HS-810

Pin Connection

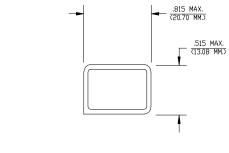
1 N.C.

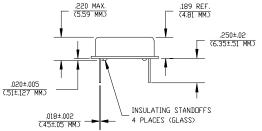
 $V_{\rm EE}$ -5.2V

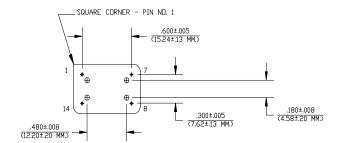
8 Output

14 V_{CC}/Ground

- Low Jitter
- MECL 10K and 10KH series compatible output on Pin 8
- High Q Crystal actively tuned oscillator circuit
- Power supply decoupling internal
- No internal PLL avoids cascading PLL problems
- High frequencies due to proprietary design
- Gold plated leads Solder dipped leads available upon request







Dimensions are in inches and (MM)



HS-800/810 Series Continued

Rev. K

Operating Conditions and Output Characteristics

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max
Frequency			15.0MHz		250.0MHz
Duty Cycle		@ V _{cc} -1.29V	45/55%		55/45%
Logic 0 (2)	V_{OL}		V _{cc} -1.95V		V _{cc} -1.60V
Logic 1 ⁽²⁾	V _{OH}		V _{CC} -1.02V		V _{CC} -0.74V
Rise & Fall Time	tr,tf 20-80%	$20-80\%V_0$ with 50 ohm load to $V_{cc}-2V$		1.0 ns	1.Š ns
Jitter, RMS ⁽³⁾					5 psec
Frequency Stability (1)	volta	all conditions including: ge, calibration, temp., aging, shock, vibration	-100ppm		+100ppm

General Characteristics

Parameter Supply Voltage	Symbol V _{EE}	Conditions	Min -5.46V	Typical -5.2V	Max -4.94V		
Supply Current	I _{EE}	50 ohm termination To 2.00V below V _{cc}	0.0 mA		80 mA		
Output current	I _O	Low level Output Current	0.0 mA		±50.0 mA		
Operating temperature	Τັ _Δ		0°C		70°C		
Storage temperature	Ts		-55°C		125°C		
Power Dissipation	P_{D}°				437 mW		
Lead temperature	T, S	Soldering, 10 sec.			300°C		
Load	50 Ohm to V _{cc} -2V or Thevenin Equivalent, Bias Required						
Start-up time	t_{s}			2 ms	10 ms		

Environmental and Mechanical Characteristics

Mechanical Shock Per MIL-STD-202, Method 213, Condition E Thermal Shock Per MIL-STD-833, Method 1011, Condition A

Vibration 0.060" double amplitude 10 Hz to 55 Hz, 35g's 55Hz to 2000 Hz

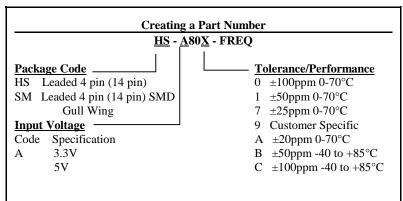
Soldering Condition 300°C for 10 seconds

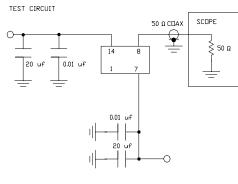
Hermetic Seal Leak rate less than 1 x 10⁻⁸ atm.cc/sec of helium

ESD Sensitivity Human Body Model per ON Semiconductor 10kH series ECL: 500V min.

Footnotes:

- 1) Standard frequency stability (±20,±25,±50ppm & others available)
- 2) V_{OL} , V_{OH} , referenced to ground (V_{CC}) with $V_{EE} = -5.2V$
- 3) Jitter performance is frequency dependent. Please contact factory for full characterization.





TEST CIRCUIT USES A SPLIT SUPPLY OF +2V AND -3.2V FOR EASE OF TESTING.



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